On the Commutation Process of Capacitor Commutated Converter (CCC)

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Abstract: Series compensated commutation is obtained by insertion a capacitor between the converter transformer and the six pulse thyristorized Graetz bridge. This arrangement will provide a reduction in the impedance of the commutation circuit. This can permit greater efficiency of commutation, reducing the reactive power consumption of the converter and the consequent necessity of large shunt compensation banks. An improvement in the susceptibility of the converter to commutation failure is achieved, when the converter is operating in the inverter mode. In this paper the valve commutation process will be studied and discussed taking into account the use of series compensated capacitor.

Key-Words: Capacitor Commutated Converter, Series Compensation, Commutation Process.

1 Introduction

There is a HVDC project, in which series compensated capacitors have been already used, as for instance, the 12 pulse [HVDC] project, Argentina – Brazil 1000 [MW] interconnection [3].

For obtaining 12-pulse converter supplying transformers whose secondary voltages are displaced thirty degrees with relation to each other, may be used. These transformers can be, for instance, transformers YY(0^o) and Y∆(30^o), which supply the bridges. Transformers star/extended-delta $(\pm 15^{\circ})$ have also already been used in the HVDC 12-pulse converter project 500 [MW], Vindyachal, India, 1988.

2 The commutation process considering the use of series compensated capacitor

Figure 1 shows the series compensated converter arrangement.

Fig. 1 – Series compensated converter.

The relative voltage drop $d_x \ell$ due to the inductance L_K is defined as [1]:

$$
\mathbf{d}_{x\ell} = \frac{3}{\pi} \mathbf{\omega} \cdot \mathbf{L}_{k} \cdot \frac{\mathbf{Id}}{\mathbf{U}_{\text{dio}}} \tag{1}
$$

where:

ω: Angular frequency.

Id: DC output current.

- U_{dio}: DC output voltage for firing angle α equal to zero degrees.
- L_K : Transformer inductance.

The relative voltage drop dx_C due to the series capacitor C_K is given by [1]:

$$
d_{XC} = \frac{3}{\pi} \cdot \frac{1}{\omega C_K} \cdot \frac{Id}{U_{div}}
$$
 (2)

These expressions have been derived from the equivalent circuit of the bridge [4]:

$$
U_{d} = U_{div} \cos \alpha - \frac{3}{\pi} X \, Id \tag{3}
$$

where:

 U_d : DC output voltage of the bridge.

X: Commutation reactance per phase.

 α : Firing angle of the bridge

One way to calculate the angle of overlap (μ) refers to the analysis of the voltage time integral during the commutation time and use the fact that the time integral of the voltage across the two commutation

inductances must be equal to $2L_K Id$ [1]. This gives: (commutation from valve 5 to 1).

$$
2L_{K}Id = \frac{1}{\omega} \int_{\alpha}^{\alpha + \mu} (u_{\text{ao}} + u_{\text{Ca}} - u_{\text{Co}} - u_{\text{Ckc}}) d\omega t
$$
\n(4)

where:

The integral of expression (4) can be split into two parts. The first one corresponding to the contribution from the source voltage and the other from the capacitor voltages. The first part is given by [1]:

$$
\frac{1}{\omega} \int_{\alpha}^{\alpha+\mu} (u_{\alpha0} - u_{\text{Co}}) \, d\omega \, t = \frac{1}{\omega} \cdot \frac{\pi}{3} U_{\text{dio}} \left[(\cos \alpha - \cos(\alpha + \mu)) \right] \tag{5}
$$

The second part can be determined [1].

$$
\frac{1}{\omega} \int_{\alpha}^{\alpha+\mu} (u_{Cka} - u_{Ckc}) d(\omega t) = \frac{1}{\omega} \cdot \frac{2\pi}{3} (1 - \frac{\mu}{2\pi}) \cdot \mu \frac{Id}{\omega C_K}
$$
\n(6)

This integral has been obtained assuming that the voltage u_{Cka} and u_{Ckc} are linear during the interval $i_a = I_d$ and $i_c = I_d$ and constant when these currents $(i_a \text{ and } i_c)$, remain null. Also, the commutation currents have been considered linear, during the commutation process.

From equations (4) , (5) and (6) , results:

$$
\frac{3}{\pi} \omega L_{\kappa} \frac{Id}{U_{\text{dio}}} = \frac{1}{2} \left[\cos \alpha - \cos(\alpha + \mu) \right] + \frac{\mu}{\omega C_{\kappa}} (1 - \frac{\mu}{2\pi}) \frac{Id}{U_{\text{dio}}} \tag{7}
$$

The determination of the commutation angle μ can be made using the Newton-Raphson method, as follows:

$$
F(\mu) = \frac{3}{\pi} \omega L_{\kappa} \frac{I_{d}}{U_{dio}} - \frac{1}{2} [\cos \alpha - \cos(\alpha + \mu)]
$$

$$
-\frac{\mu}{\omega C_{\kappa}} (1 - \frac{\mu}{2\pi}) \frac{I_{d}}{U_{dio}}
$$
(8)

$$
F'(\mu) = -\frac{1}{2}sen(\alpha + \mu) - \frac{1}{\omega C_K} \cdot \frac{I_d}{U_{di\omega}} \left(1 - \frac{\mu}{\pi}\right)
$$
(9)

The Newton-Raphson method is applied as follows:

$$
\mu_{i+1} = \mu_i - \frac{F(\mu_i)}{F'(\mu_i)}
$$
\n(10)

Considering the expressions (1) and (2), expression (7) can be written as:

$$
\mathbf{d}_{X\ell} = \frac{1}{2} \left[\cos \alpha - \cos(\alpha + \mu) \right] + \frac{\pi}{3} \cdot \mu \left(1 - \frac{\mu}{2\pi} \right) \mathbf{d}_{X\ell} \tag{11}
$$

and expression (8) as:

$$
F(\mu) = d_{x\ell} - \frac{1}{2} \left[\cos\alpha - \cos(\alpha + \mu) \right] - \frac{\pi}{3} \cdot \mu \left(1 - \frac{\mu}{2\pi} \right) d_{xc}
$$
\n(12)

The interactive process is continued until the expression $ABS(F(\mu))$, (8) remains lower-equal of a given maximum tolerance value (TOL). The developed program flowchart using the Newton-Raphson method, is shown in figure 2.

Fig. 2 – Developed program flowchart.

The expression of the commutation angle (μ) without the use of series capacitor is given by [4]:

$$
I_d = I_{s2}(\cos\alpha - \cos\delta)
$$
 (13)

$$
\delta = \alpha + \mu \tag{14}
$$

$$
I_{S2} = \frac{\sqrt{3} \text{ Em}}{2 \omega L_{K}}
$$
 (15)

Where Em: Peak value of the phase-neutral converter supply voltage.

Results:

$$
\mu = \cos^{-1} \left[\cos \alpha - \frac{Id}{I_{s2}} \right] - \alpha \tag{16}
$$

3 Obtained Results

Considering the transformer with the following data:

 $S = 2$ [KVA] (rated power) U_2 = 190 [V] (rated secondary voltage) X_{LK} % = 8% (reactance)

Results:

 X_{LK} = 0.08 x (190)²/2000

 $X_{LK} = 1.44 [\Omega]$ (17)

Where $X_{LK} = \omega L_K$ (18)

3.1 Bank Dimensioning

The characteristic U_d x I_d of CCC is given by [1]:

$$
U_{di} = U_{dio} \left[\cos \alpha - \left(d_{x \ell N} - \frac{\pi}{3} \cdot \mu \cdot \left(1 - \frac{\mu}{2\pi} \right) d_{x e N} \right) \cdot \frac{I_d}{I_{dN}} \cdot \frac{U_{diol}}{U_{dio}} \right] \tag{19}
$$

Where the index N is related with rating conditions of the considered term. For instance, considering $d_{x \in N}/d_{x \in N} = 4$ and $\mu =$ 15[°] (15 π /180 rad), which is a satisfactory value for HVDC systems, results in a slope characteristic U_d x I_d almost equal to zero. So, the output voltage will be practically constant, independent of the DC current.

Results (according to [4]):

$$
S = 1.047 U_{\text{dio}} I_{\text{d}} \tag{20}
$$

$$
U_{\rm dio} = 1.35 \, U_2 \tag{21}
$$

$$
I_{dN} = \frac{2000}{1.047x1.35x190}
$$

\n
$$
I_{dN} = 7.45[A]
$$

\n
$$
I_1 = \frac{\sqrt{6}}{\pi} I_d
$$
 (22)

$$
I_1 = 5.81 [A]
$$
 (23)

$$
X_{CK} = 4X_{LK}
$$
 (24)

 X_{CK} = 5.76[Ω], resulting:

$$
C_K = 460.5 \,[\mu\,\text{F/per phase}] \tag{25}
$$

For this value of C_K and taking into account rating conditions, the commutation angle for $\alpha = 155^{\circ}$ will be calculated, using the developed program (flowchart figure 2):

 $\mu \geq 6.5^\circ$

For the conventional converter, without capacitor, the angle μ is:

 $\mu \approx 15.5^{\circ}$ (using (16)).

The reactive power generated by the capacitors for rating conditions is:

$$
Q_{CN} = 3X_{CK} I_1^2
$$
 (26)
Q_{CN} = 583.3[VAr]

According to [1] α_{\min} can be calculated for rating conditions, and considering the commutation angle $\mu = 6.5^{\circ}$.

$$
\sin \alpha_{\min} = -\frac{2X_{CK} I_d}{U_{di\text{o}}} \left(1 - \frac{3\mu}{4\pi} \right) \tag{27}
$$

 $\sin\alpha_{\min} = -0.3255$

 $\alpha_{\min} = -19^{\circ}$ (28) $(considering μ)$

Neglecting the commutation angle $(\mu = 0)$. $\sin\alpha_{\min} = -0.3347$

$$
\alpha_{\min} = -19.55^{\circ}
$$
 (29)
(Neglecting (μ)).

3.2 Inverter Commutation Failures

The more common inverter commutation failures of the conventional converter are shown in figures 4, 5, 6 [5]. These failures are minimized in the CCC converter, consisting, so, this fact, an advantage of the CCC converter.

Figure 3 shows the conventional DC inverter output voltage for a single commutation failure (valves 5 and 3).

Fig. 3 – Inverter DC output voltage for single commutation failure.

Figure 4 shows the inverter DC output voltage for double successive commutation failure (valves 5-3; 6-4).

Fig. 4 – Inverter DC output voltage for double successive commutation failure (valves 5-3; 6-4).

Figure 5 shows the inverter DC output voltage for double non successive commutation failure (valves 5-3; 2-6).

Fig. 5 – Inverter DC output voltage for double non successive commutation failure (valves 5-3; 2-6).

3.3 Operation as Rectifier

Table 1 shows the commutation angle (μ) for rectifier operation, and considering, for instance, $I_d = 0.5$ [A].

Table 1: CCC Commutation angle (μ) for rectifier operation.

3.4 Operation as Inverter

Table 2 shows the commutation angle (μ) for inverter operation.

Firing Angle (α)	Capacitor (μF)	Commutation
		angle (μ)
α = 160 ^o		(degrees)
	50	0.57
	60	0.61
	70	0.64
	80	0.66
	90	0.69
	100	0.70
Firing Angle (α)	Capacitor (μF)	Commutation
		angle (μ)
		(degrees)
	50	0.44
α = 150 ^o	60	0.47
	70	0.48
	80	0.50
	90	0.51
	100	0.52
Firing Angle (α)	Capacitor (μF)	Commutation
		angle (μ)
		(degrees)
	50	0.37
$\alpha = 140^\circ$	60	0.38
	70	0.40
	80	0.41
	90	0.41
	100	0.42
Firing Angle (α)	Capacitor (μF)	Commutation
		angle (μ)
		(degrees)
	50	0.32
α = 130 ^o	60	0.33
	70	0.34
	80	0.35
	90	0.36
	100	0.36
Firing Angle (α)	Capacitor (μF)	Commutation
		angle (μ)
		(degrees)
	50	0.29
$\alpha = 120^{\circ}$	60	0.30
	70	0.31
	80	0.32
	90	0.32
	100	0.32

 Table 2: CCC Commutation angle for inverter operation.

3.5 Operation as Rectifier (without capacitor – conventional converter)

Table 3 shows the commutation angles (μ) of the conventional converter for rectifier operation.

Table 3: Conventional converter commutation angles (μ) for rectifier operation.

3.6 Operation as Inverter - conventional converter

 Table 4 shows the commutation angles (µ) of the conventional converter for inverter operation.

 Table 4: Conventional converter commutation angles (u) for inverter operation.

4 Conclusion

The capacitor commutated converter provides a reactive power compensation, and so large shunt filter banks are no longer required for this purpose.

Better commutation failure immunity is achieved due to the increase of the inverter commutation margin, when series capacitors are used. Also increasing the DC current, results in an increasing voltage across the capacitor, compensating this way the drop in the AC network voltage.

The developed program using the Newton-Raphson method (flowchart figure 2) for calculation of CCC commutation angles (μ) , has presented convergence in the interactive process.

The commutation angle (μ) is lower for capacitor commutated converter, when compared with the conventional no compensated converter, as seen comparing the commutation angles (μ) of tables 1 and 2 (CCC) with the commutation angles (μ) of tables 3 and 4 (no compensated converters), for each considered firing angle (α) .

The series capacitor's impedance provides also an increasing in the total

secondary side impedance, reducing, in this way, the DC side short circuit currents. The capacitor commutated converter (CCC), can operate with a good power factor. As well in some cases, it can until present to the network a leading power factor. In these cases the firing angle α is negative in the rectifier and higher than 180 degrees in the inverter.

At the moment maximum capacitors per phase 100 [µF] are available in our laboratories. Because of this fact, tables 1 and 2 present values of commutation angles for this maximum considered capacitor. However we are providing the acquisition of the rated capacitors (460 µF/per phase), which provide the operation of the CCC with rating conditions of the supply transformer $(Id_N =$ 7.45 [A]), resulting in a lower RMS capacitor voltage. The slope characteristic $(U_d \times I_d)$ being almost equal zero to the inverter, will result favorable with respect to the stability of the system.

References:

- [1]A.Ekström, "Capacitor commutated converter (CCC)", Voltage Source Converters and Series Compensated HVDC Schemes, *Cigré - Course*, Brasil CE-14, Rio de Janeiro, Brazil, 2000.
- [2]T.J.Hammons; D.Woodford; J.Loughtan; M.Chamia; J.Donahoe; D.Povh; B.Bisewski; W.Long. "Role of HVDC transmission in future energy development", *IEEE Power Engineering Review*, volume 20, number 2, february 2000, pp. 10-25.
- [3]J.Graham; D.Menzies; G.Biledt; A.R.Carvalho; W.W.Ping; A.Wey. "Electrical system considerations for the Argentina–Brazil 1000 [MW] Interconnection", *Eletroevolução-Sistemas de Potência*, (Cigré - Magazine), n° 23, june 2001, pp. 6-12.
- [4]E.W.Kimbark, "*Direct current transmission*", Vol I, Wiley Interscience, New York, 1971.
- [5]"*Converter Faults Application Notes*" CEPEL (Electrical Energy Research Center), Rio de Janeiro – Brazil – 1986.